



TECHNISCHE  
UNIVERSITÄT  
WIEN  
VIENNA  
UNIVERSITY OF  
TECHNOLOGY

Institut für Computertechnik

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Workshop on

## Interaction of Analog/Mixed-Signal Systems and Transaction Level Modeling

*Organized by the OSCI SystemC-AMS WG & Vienna University of Technology*

Location: TU Vienna, Main Building (Karlsplatz), Seminarraum 8  
Hof 2, Stiege 7, ebenerdig  
Time: 31.01.08, 9:00 – 13:00

- 9:00-9:10 *Welcome*  
Martin Barnasconi, NXP; Christoph Grimm, TU Vienna
- 9:10-9:30 *Industry perspectives*  
NXP: Martin Barnasconi, ST: Serge Scotti, Infineon: N.N.
- 9:30-9:50 A simulation framework for sensor network  
Johann Glaser, Vienna University of Technology
- 9:50-10:15 Coffee break&Discussion
- 10.15-11:15 *Timed TLM*  
Francois Pecheux
- 11.15-11:30 *Converter Channels*  
Markus Damm, Vienna University of Technology
- 11:30-11:45 *Interfacing cycle-accurate TLM Models and analog mixed-signal SDF Clusters in SystemC - A Case Study*  
Andreas Herrholz, OFFIS
- 11:45-12:15 *Performance Analysis and Automated C++ Modularization using Module-Adapters for SystemC*  
Nico Bannow, Tobias Kirchner; Robert Bosch Corporate Research
- 12:15-13:00 Discussion